

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOS = 2.0~4.0 V (V

DS = 10 V, I<sub>D</sub> = 1 mA)

## Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V <sub>DSS</sub>	75	V
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)	V <sub>DGR</sub>	75	V
Gate-source voltage	V <sub>GSS</sub>	±20	V
Drain current	DC (Note 1)	I <sub>D</sub>	80
	DC (Note 1,4)	I <sub>D</sub>	70
	Pulse (Note 1)	I <sub>DP</sub>	240
Drain power dissipation (T <sub>c</sub> = 25°C)	P <sub>D</sub>	200	W
Single pulse avalanche energy (Note 2)	E <sub>AS</sub>	107	mJ
Avalanche current	I <sub>AR</sub>	40	A
Repetitive avalanche energy (Note 3)	E <sub>AR</sub>	20	mJ
Peak diode recovery dv/dt (Note 5)	dv/dt	12	V/ns
Channel temperature (Note 4)	T <sub>ch</sub>	175	°C
Storage temperature range (Note 4)	T <sub>stg</sub>	-55~175	°C

## Thermal Characteristics

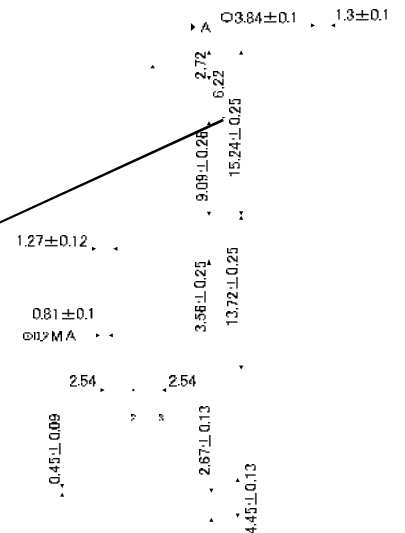
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	0.75	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	83.3	°C / W

Note 1: Ensure that the channel temperature does not exceed 175°C.

Note 2: V<sub>DD</sub> = 25 V, T<sub>ch</sub> = 25°C (initial), L = 100 μH, R<sub>G</sub> = 25 Ω, I<sub>AR</sub> = 40A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

Note 4: T<sub>c</sub>=100



## Electrical Characteristics (Ta = 25°C)

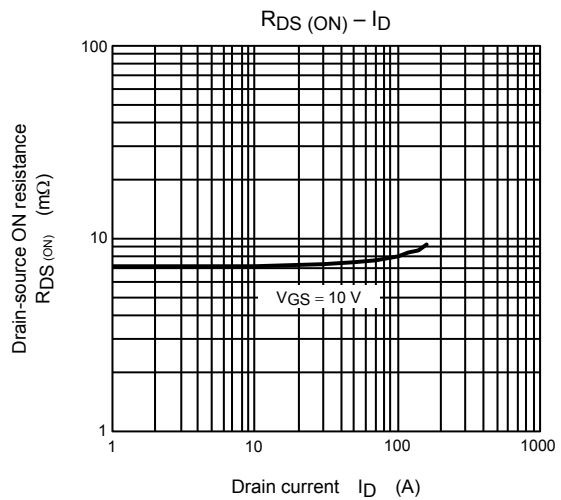
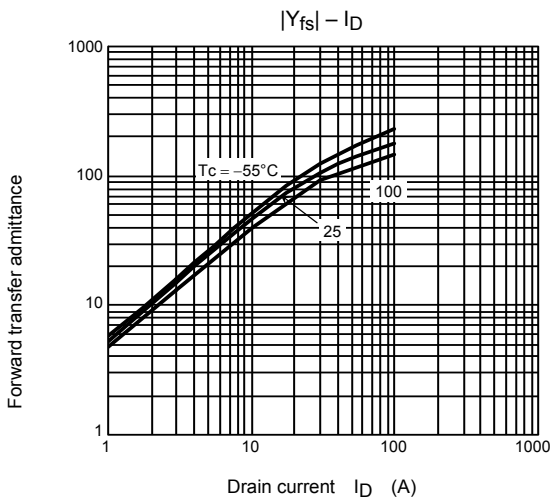
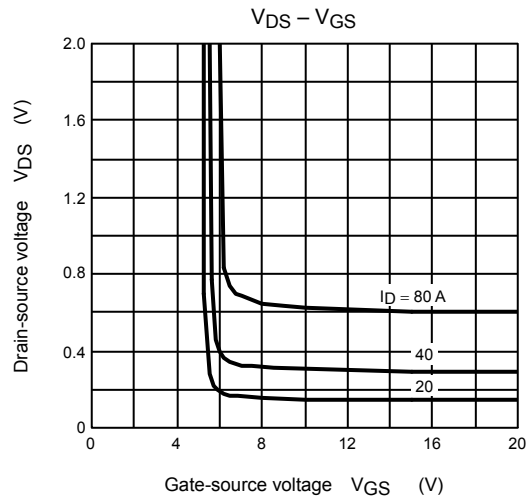
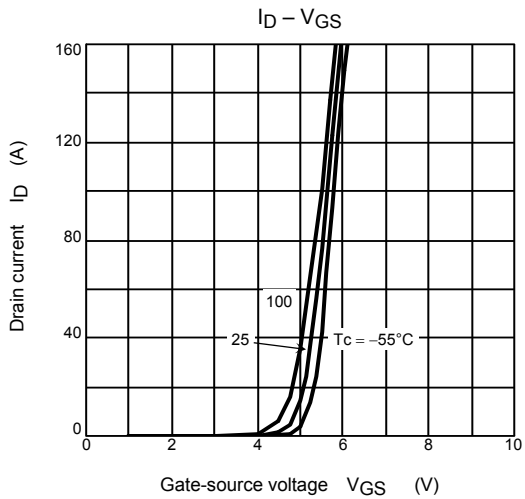
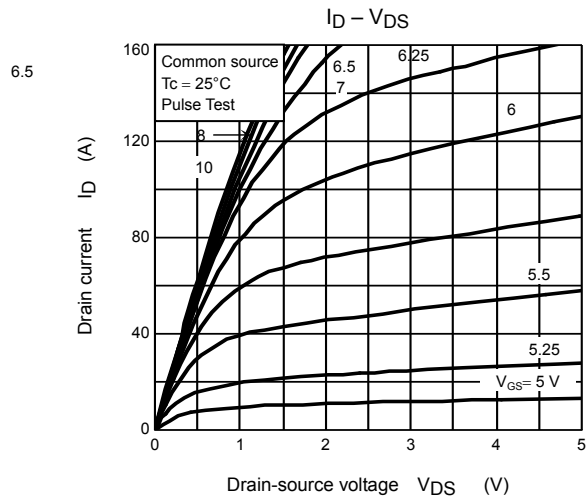
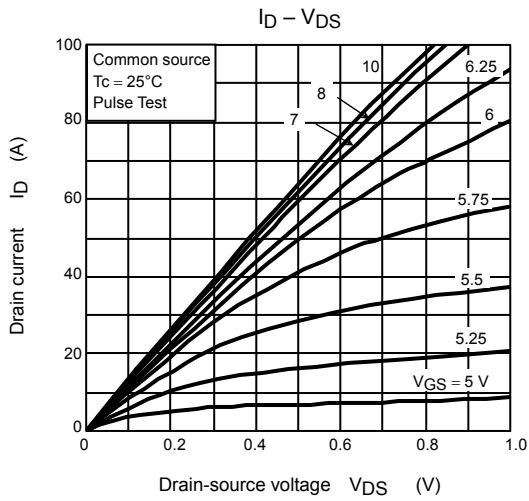
Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		$I_{GSS}$	$V_{GS} = \pm 20\text{ V}, V_{DS} = 0\text{ V}$	—	—	$\pm 1$	$\mu\text{A}$
Drain cut-off current		$I_{DSS}$	$V_{DS} = 75\text{ V}, V_{GS} = 0\text{ V}$	—	—	10	$\mu\text{A}$
Drain-source breakdown voltage		$V_{(BR)DSS}$	$I_D = 10\text{ mA}, V_{GS} = 0\text{ V}$	75	—	—	V
		$V_{(BR)DSX}$	$I_D = 10\text{ mA}, V_{GS} = -20\text{ V}$	45	—	—	V
Gate threshold voltage		$V_{th}$	$V_{DS} = 10\text{ V}, I_D = 1\text{ mA}$	2.0	—	4.0	V
Drain-source ON resistance		$R_{DS(ON)}$	$V_{GS} = 10\text{ V}, I_D = 40\text{ A}$	—	7.5	9.0	$\text{m}\Omega$
Forward transfer admittance		$ Y_{fs} $	$V_{DS} = 10\text{ V}, I_D = 40\text{ A}$	67	135	—	S
Input capacitance		$C_{iss}$	$V_{DS} = 10\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$	—	3600	—	pF
Reverse transfer capacitance		$C_{rss}$		—	350	—	
Output capacitance		$C_{oss}$		—	500	—	
Switching time	Rise time	$t_r$		—	16	—	ns
	Turn-on time	$t_{on}$		—	33	—	
	Fall time	$t_f$		—	13	—	
	Turn-off time	$t_{off}$		Duty $\leq 1\%$ , $t_w = 10\ \mu\text{s}$	—	63	
Total gate charge (Gate-source plus gate-drain)		$Q_g$	$V_{DD} \approx 60\text{ V}, V_{GS} = 10\text{ V}, I_D = 80\text{ A}$	—	75	—	nC
Gate-source charge		$Q_{gs}$		—	44	—	
Gate-drain ("miller") charge		$Q_{gd}$		—	31	—	

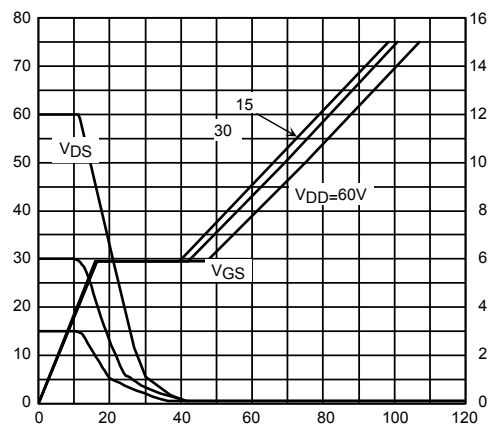
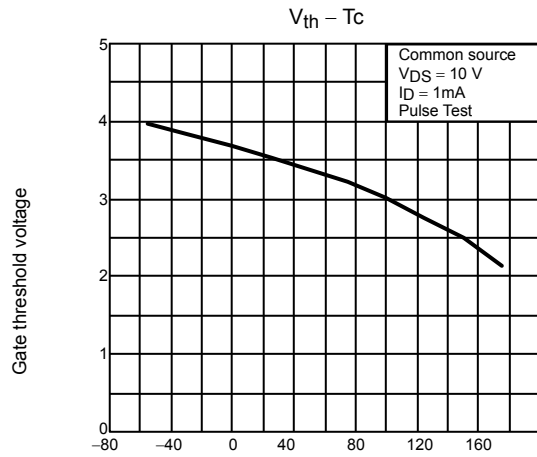
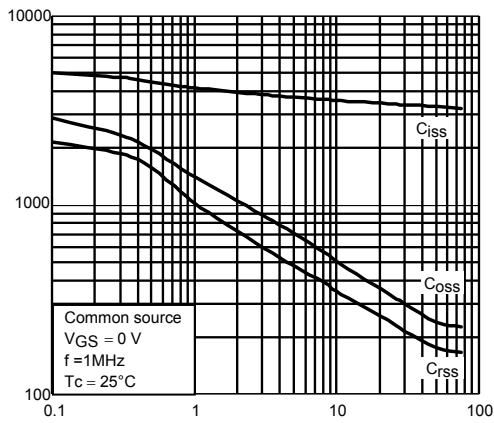
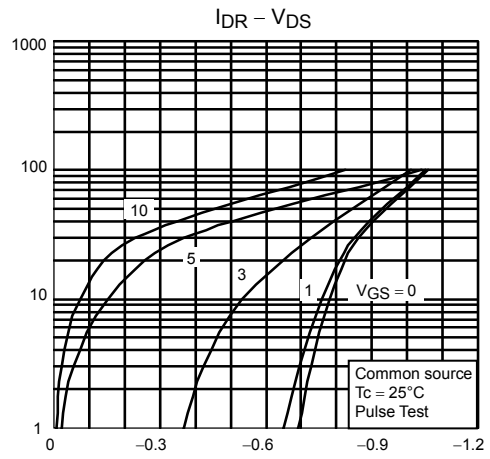
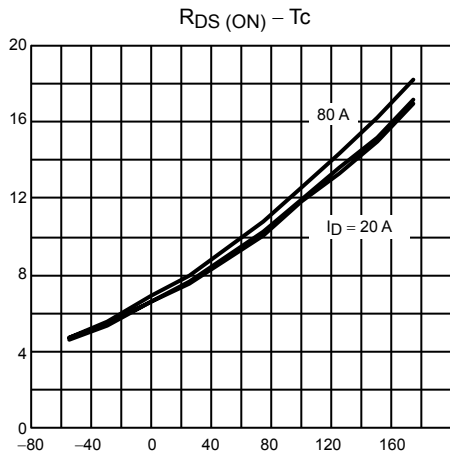
## Source-Drain Ratings and Characteristics (Ta = 25°C)

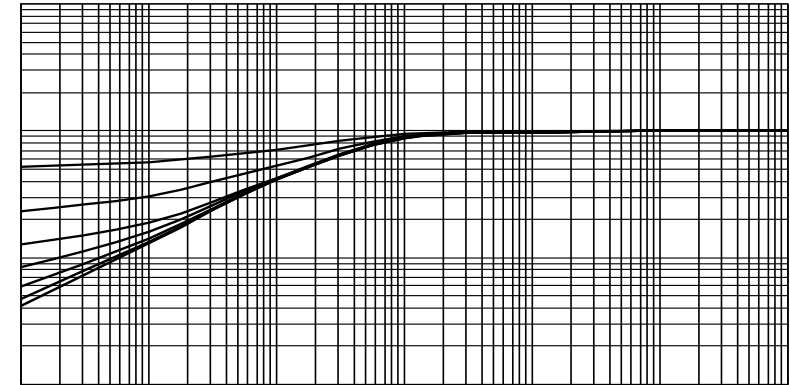
Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Continuous drain reverse current (Note 1)	$I_{DR}$	—	—	—	80	A
Pulse drain reverse current (Note 1)	$I_{DRP}$	—	—	—	240	A
Forward voltage (diode)	$V_{DSF}$	$I_{DR} = 80\text{ A}, V_{GS} = 0\text{ V}$	—	—	-1.5	V
Reverse recovery time	$t_{rr}$	$I_{DR} = 80\text{ A}, V_{GS} = 0\text{ V}$	—	45	—	ns
Reverse recovery charge	$Q_{rr}$	$dI_{DR} / dt = 100\text{ A} / \mu\text{s}$	—	72	—	$\mu\text{C}$

## Marking

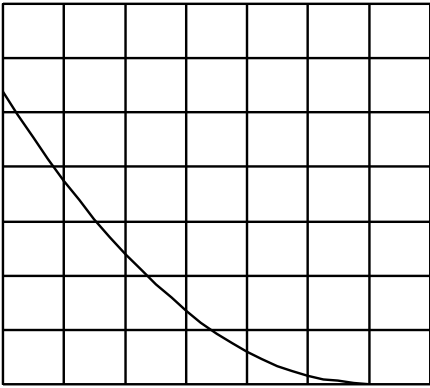
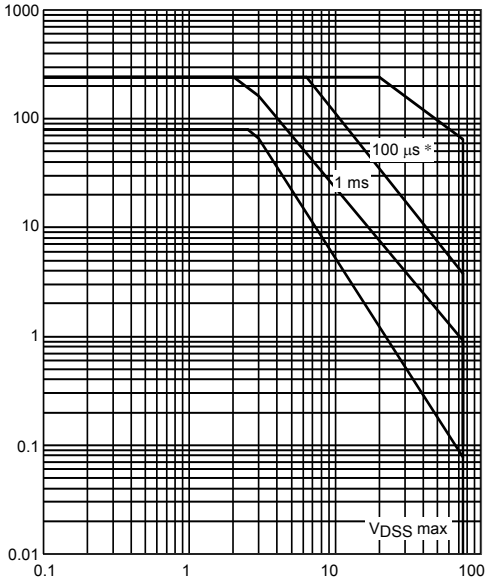
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		Part No. (or abbreviation code) Lot No.	Lot No. Part No. (or abbreviation code)







10



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